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## APPLICANTS

Rochit Rajsuman, Santa Clara, CA;

Robert Sauer, Santa Clara, CA;  
Hiroki Yamoto, Santa Clara, CA;\*\* CONTINUING DATA \*\*\*\*\* *yes pme*This application is a CIP of 10/340,349 01/10/2003  
which is a CIP of 09/547,752 04/12/2000 PAT 6,567,941\*\* FOREIGN APPLICATIONS \*\*\*\*\* *none pme*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/23/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 13	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged	<i>pme</i> Examiner's Signature	Initials	

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## TITLE

Semiconductor test system storing pin calibration data, commands and other data in non-volatile memory

FILING FEE

FEES: Authority has been given in Paper  
No. \_\_\_\_\_ to charge/credit DEPOSIT ACCOUNT

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees ( Filing )
<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )